405-625 STANDARDS DETECTOR UN20/513

The UN20/513 accepts a composite video signal on either the 405-line standard or the 625-line standard and operates a relay if the input signal is on a preselected standard. Standard selection is made by means of a soldered wire link. The detector operates at input levels down to −10 dB relative to 1 volt p-p.

The UN20/513, which also contains a PS2/22C, is constructed on a CH1/12A chassis with index-peg positions 16 and 32.

The circuit of the UN20/513 is given overleaf in Fig. 1. The input signal is fed to a simple sync separator, transistor TR5, via a chrominance trap. Positive-going sync pulses are fed from the collector of transistor TR5 to a circuit tuned to line-frequency. The resulting sinusoidal signal is d.c. restored and detected to operate a relay RLA. Zener diode D5 biases transistor TR8 to prevent the relay operating with low-level or wrong line-standard input signals.

MJR 3/68

See overleaf for Fig. 1

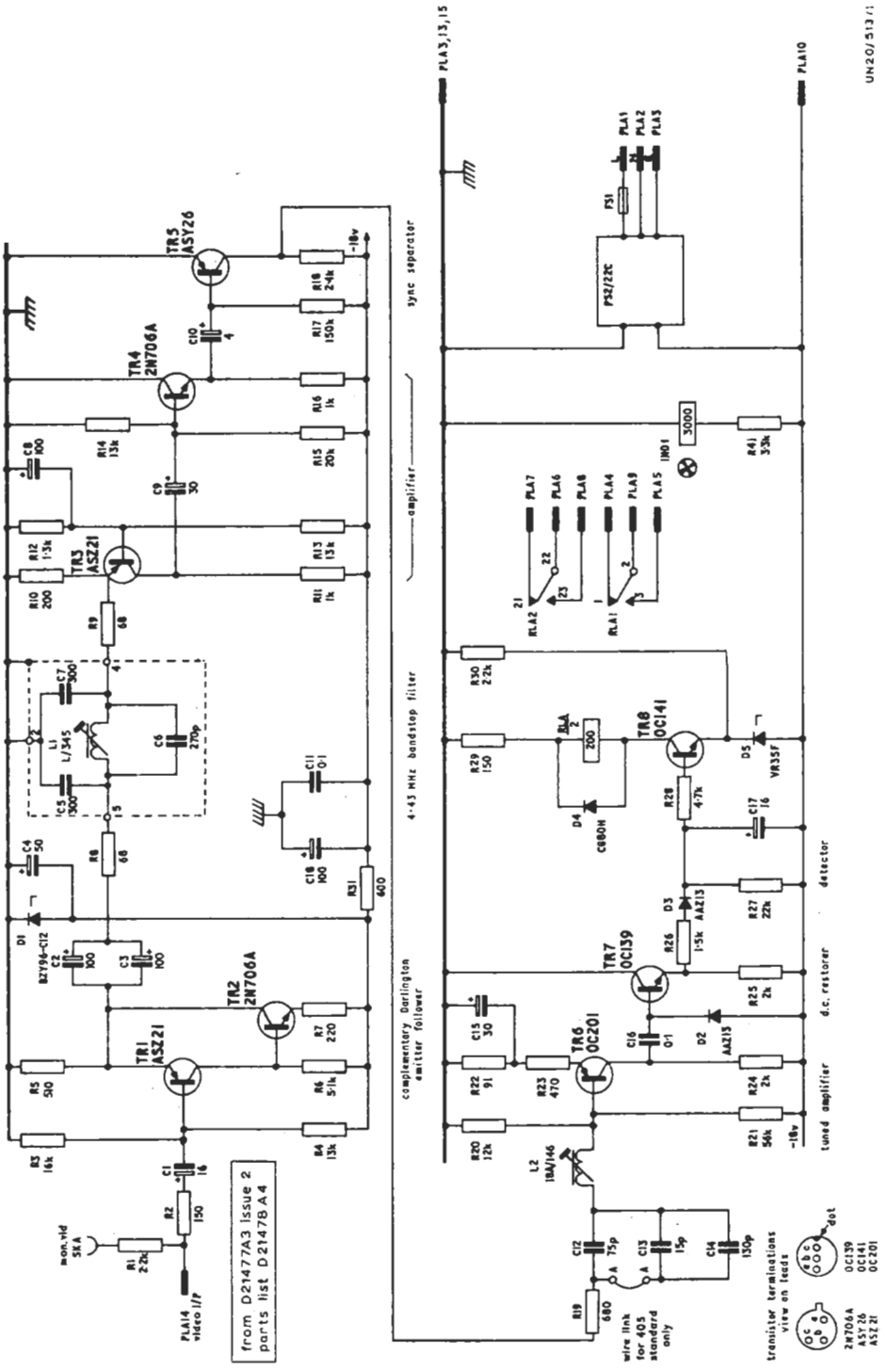

Fig. 1 Circuit of 405—625 Standards Detector UN20/513