SECTION 22

DELAY NETWORK NE4/503

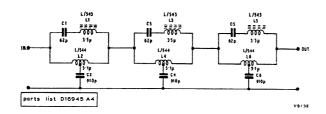


Fig. 22.1 Circuit of the NE4/503

The NE4/503 is a three-section delay network of the type described in Designs Department Technical Memorandum No. 9.19(61). The total delay of the NE4/503 is about $0.2~\mu s$. It is constructed on a printed wiring board 2.5~ins. by 3.5~ins.

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