

AMPLIFIER DR/1

Drawing A.2998, Issue 5

This is a disc recording amplifier used at Maida Vale. Its input is connected to the programme source and its output to the input of the power amplifier DRP/I which feeds the cutter head. The DR amplifier incorporates the frequency correction circuits designed, partly to reduce the low frequency response in order to prevent the cutter head from breaking through the walls between adjacent grooves in the record, and partly to provide correction for the non-linearity of the particular cutter head in use. The cutter head correction circuit is arranged as a self-contained unit having components of values specially designed for the particular cutter head and can be readily changed if the cutter head in use is changed.

Circuit

It is a three-stage resistance coupled amplifier with a screened input transformer and with the output of the final stage choke-capacity coupled to the output transformer. A monitoring output for headphones is provided by tapping across the output from the first stage. The volume control is provided in the input to the first stage, the bass-cutting correction circuit is connected in the input to the second stage and the circuit providing correction for the particular cutter head in use is connected to the input to the final stage. The grid bias is automatic.

Impe	dances										
	Input impedance				·				(approx)	1,200	ohms
Output impedance								(approx)	180	\mathbf{ohms}	
	\mathbf{Normal}	load ir	npedar	nce	•••	• •		• •	(approx)	620	\mathbf{ohms}
Transformers						Number		Impedance Ratio		Turns Ratio	
	\mathbf{Input}	• •	••				$54\\193$		$1/10.9 \\ 16/1$		1/3.31 4/1
	Output	• •									
						1					

AMPLIFIER DR/1 Technical Instructions

Item 3 (DR/1). July, 1938

Volume	Control								
		<i>Type</i> P 10	7 Res 100	Potal sistance ,000 Ω	No. oj Studs 10	f 3	Loss per Stud 4 db. each stud	Los Lowe In	ss on est Stud finite
Supply]	Data								
				Automa	tic				
	Stage	Val	ve	Grid Bi	as	Anod	e Current	2	Filaments
			V	olts nega	ative	$\mathbf{m}\mathbf{A}$	(approx)	Vo	lts Amps
	1	ACHL	1	2.4			3.2		4 1
	2	ACP		5.6			7.5		4 1
	3	ACP 1		30			20		4 1
				Total		30.7			3
${}^{\circ}\mathrm{High}$	Tension	Supply							
	H.T. + 1	(Stages	1 & 2)				(approx)	275V	rectified A.C.
	H.T. $+2$	(Output	t stage)				(approx)	230V	rectified A.C.
Low	Tension	Supply		V abžel	-		(approx)	6V (a	djusted to $4V$
								by ser	ies resistance)
Working Testi	yoltage Volume of outp Second st conn Output la Level at	e Gain itions control s ut. tage of ection fr oaded wi input, at	et in po correction from D to ith 600 of all test	sition for f_{0} short- o E).	or maxin circuited cies, adju	num (by sted			
0.1	to -	-14 db.					26	11. 0	
Gain	1 at 1,00		•• ••	••	•••	•••	30 ±	2 ab.	\backslash
Gam	at 50	c/s.	•••••••	••		••	-11.5 ±	1 db	
	250	c/s.	• •	••		•••	$-3.0 \pm$	1 db	
	200 500	0/8.	•••			• •	$-1.2 \pm$	1 db	
	2 000	0/8.	••••••	• •	••	•••	$^{-1.3}\pm$	1 dh	Relative to
	2,000	0/8.	•••••	••	••	•••	$^{\pm 2.0} \pm$	1 db	gain at
	4 000	0/8.	••••••		••	•	$\pm 4.7 \pm$	1 db	
	5,000	c/s.	••••••		•••		$\pm 5.8 \pm$	1 db	1,000 0/5
	5,000 6,000	c/s.	•• ••	••	••	••	$+9.0 \pm$	1 db	
	0,000	c/s.	•• ••	••	••	••	$+0.1 \pm$	1 db.	
	7,000	c/s.	•••••••	••	••	••	$+1.1 \pm$	1 JL)
	8,000	c/s.	••••••	•••	••		$+0.0 \pm$	1 ab. /	